

SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)

(Sponsored by Sri Vasavi Educational Society)

(Approved by AICTE, New Delhi & Recognized by UGC under section 2(f) & 12(B))
 (Permanently affiliated to JNTUK, Kakinada, Accredited by NBA and NAAC with 'A' Grade)
 Pedatadepalli, TADEPALLIGUDEM – 534 101, W.G. Dist. (A.P)

Department of Electronics and Communication Engineering

Ref. No: SVEC/ECE/2022-23/52

Date: 20.01.2023

CIRCULAR

This is to inform to all faculty members and students that we are going to conduct two days Hands-on Workshop on “**Design & Analysis of Antenna using CST Microwave Studio Software tool**” during 24th & 25th of January 2023. The Interested Faculty members and Final year students can attend the workshop.

Schedule of the Two day Program:

S. No	Date	Timings	Name of the Topic	Instructor Details
1	24-01-2023	10.30 AM to 1 PM	Edge feeding Patch antenna using CST software	M. Vinod Kumar, Asst Professor, ECE Dept
2		2 PM to 4 PM	Design and Simulation of Inset feed Patch antenna using CST software	
3	25-01-2023	10.30 AM to 1 PM	Design and Simulation of Probe feed Patch antenna using CST software	
4		2 PM to 4 AM	Introduction to ADS Software	

Head of the Department

Vision

- To develop the department into a centre of excellence and produce high quality, technically competent and responsible Electronics and communication engineers

Mission

- To create a learner centric environment that promotes the intellectual growth of the students.
- To develop linkages with R & D organizations and educational institutions for excellence in teaching, learning and consultancy practices.
- To build the student community with high ethical standards.



